

# Modular Control

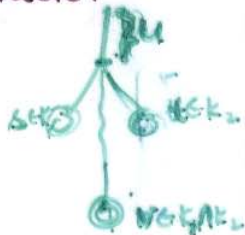
- Desired behavior  $K = K_1 \cap K_2$ 
  - buffer should not overflow / underflow:  $K_1$
  - downtime m/c has priority of repair:  $K_2$

- Modular approach: Design  $S_i$  s.t.  $G \| S_i$  meets  $K_i$   
Then construct  $S := (S_1 \| S_2) \stackrel{?}{\Rightarrow} G \| S$  meets  $K$

- Computational advantage:  $O(\min_1^2) + O(\min_2^2) \quad \underline{vs} \quad O(\min_1 \min_2^2)$ .

- Def:  $K_1, K_2$  modular if  $[\text{pr}(K_1) \cap \text{pr}(K_2)] = \text{pr}(K_1 \cap K_2) \iff \text{pr}(K_1) \cap \text{pr}(K_2) \subseteq \text{pr}(K_1 \cap K_2)$  (non-conflicting)
- clearly this holds for  $K_1, K_2$  prefix-closed.
- $K_1, K_2$  share a prefix  $\Rightarrow$  they share a trace containing that prefix.
- Lemma:  $K_1, K_2$  modular,  $K_1, K_2$  controllable  $\Rightarrow K_1 \cap K_2$  controllable.

$$\begin{aligned} \text{pr}(K_1 \cap K_2) \Sigma_u \cap L(G) &\subseteq [\text{pr}(K_1) \cap \text{pr}(K_2)] \Sigma_u \cap L(G) \\ &= [\text{pr}(K_1) \Sigma_u \cap L(G)] \cap [\text{pr}(K_2) \Sigma_u \cap L(G)] \\ &\stackrel{\text{controllability}}{\subseteq} \text{pr}(K_1) \cap \text{pr}(K_2) \stackrel{\text{modularity}}{=} \text{pr}(K_1 \cap K_2) \end{aligned}$$



- Modular control is always possible for prefix-closed languages.
- Thm: Given  $G, K_1, K_2$ . Suppose exists  $\Sigma_u$ -enabling, non-marking  $S_i$  s.t.  $L(G \| S_i) = K_i$ . Then  $S := S_1 \| S_2$  is  $\Sigma_u$ -enabling, non-marking and  $L(G \| S) = K_1 \cap K_2$ .
- Must have  $K_i = \text{sup PC}(K_i)$ ; otherwise  $S_i$  s.t.  $L(G \| S_i) = \text{sup PC}(K_i)$
- Thm:  $\text{sup PC}(K_1) \cap \text{sup PC}(K_2) = \text{sup PC}(K_1 \cap K_2)$

## Modular Control : Non-prefix closed case

• Thm: Given  $G, K_1, K_2$ . Suppose exists  $\Sigma$ -enabling, non-marking, nonblocking  $S_i$  s.t.  $L_m(G||S_i) = K_i$ . Then  $S = S_1 || S_2$  is  $\Sigma$ -enabling, non-marking and  $L_m(G||S) = K_1 \cap K_2$ . Furthermore,  $S$  is non-blocking if and only if  $K_1, K_2$  are modular.

Pf:  $S_i$   $\Sigma$ -enabling  $\Rightarrow L(G||S_i)$  controllable  
 $\Rightarrow L(G||S) = L(G||S_1) \cap L(G||S_2)$  controllable  
 $\Rightarrow S$   $\Sigma$ -enabling

$S_i$  non-marking  $\Rightarrow L_m(G||S_i) = L(G||S_i) \cap L_m(G)$   
 $\Rightarrow L_m(G||S) = L_m(G||S_1) \cap L_m(G||S_2) = [L(G||S_1) \cap L_m(G)] \cap [L(G||S_2) \cap L_m(G)]$   
 $= L(G||S) \cap L_m(G) \Rightarrow S$  non-marking

$$L_m(G||S) = L_m(G||S_1) \cap L_m(G||S_2) = K_1 \cap K_2$$

$$L(G||S) \supseteq L(G||S_1) \cap L(G||S_2) = \text{pr}(L_m(G||S_1)) \cap \text{pr}(L_m(G||S_2)) = \text{pr}(K_1) \cap \text{pr}(K_2)$$

$$S \text{ non-blocking} \Leftrightarrow \text{pr}(L_m(G||S)) = L(G||S)$$

$$\Leftrightarrow \text{pr}(K_1 \cap K_2) = \text{pr}(K_1) \cap \text{pr}(K_2) \Leftrightarrow (K_1, K_2) \text{ modular.}$$

Complexity: "non-modular" approach: controllability & Relative-closure of  $K_1 \cap K_2$   
 $\Rightarrow O(mn_1 n_2) \sim O(n^3)$

"modular" approach: controllability + relative-closure of  $K_i$ , modularity of  $K_1, K_2$   
 $\Rightarrow O(mn_1) + O(mn_2) + O(n_1 n_2) \sim O(n^2)$

• Existence of  $S_i$  requires  $K_i = \text{supRC}(K_i)$ ; otherwise consider  $\text{supRC}(K_i)$

Thm:  $\text{supRC}(K_1), \text{supRC}(K_2)$  modular. Then

$$\text{supRC}(K_1 \cap K_2) = \text{supRC}(K_1) \cap \text{supRC}(K_2)$$

$$\begin{array}{l} \frac{a \quad b/c \quad d}{\quad \quad \quad} G \\ \frac{a \quad b \quad c}{\quad \quad \quad} K_1 \Rightarrow \frac{a \quad b}{\quad \quad \quad} \text{supRC}(K_1) \\ \frac{a \quad c \quad d}{\quad \quad \quad} K_2 \Rightarrow \frac{a \quad d}{\quad \quad \quad} \text{supRC}(K_2) \end{array}$$

Note:  $K_1, K_2$  modular  $\nRightarrow \text{supRC}(K_1), \text{supRC}(K_2)$  modular