

CprE 2810: Class Schedule (Fall 2025)

Week	Day/Date	Topic	Readings	Homework	Lab
1	Monday 8/25	Introduction	1.1, 1.2, 1.3, 1.4		No Lab
	Wednesday 8/27	Binary Numbers	1.5, 1.6		
	Friday 8/29	Truth Tables & Logic Gates	2.1, 2.2, 2.3, 2.4		
2	Monday 9/1	NO CLASS: University Holiday			Lab 1
	Wednesday 9/3	Boolean Algebra	2.5	HW 1 due	
	Friday 9/5	AND, OR, NOT	2.6		
3	Monday 9/8	NAND, NOR	2.7	HW 2 due	Lab 2
	Wednesday 9/10	Design Examples	2.8		
	Friday 9/12	Intro to Verilog	2.9, 2.10		
4	Monday 9/15	Karnaugh Maps	2.11	HW 3 due	Lab 3
	Wednesday 9/17	Minimization	2.12, 2.13		
	Friday 9/19	Functions and Circuits	2.14, 2.15, 2.16		
5	Monday 9/22	Examples	2.17	HW 4 due	Lab 4
	Wednesday 9/24	Midterm Review Session			
	Friday 9/26	Midterm #1			
6	Monday 9/29	Addition of Unsigned Numbers	3.1, 3.2	HW 5 due	Lab 5
	Wednesday 10/1	Signed Numbers	3.3		
	Friday 10/3	Fast Adders	3.4, 3.5		
7	Monday 10/6	Multiplication	3.6		Mini Project
	Wednesday 10/8	Floating Point Numbers	3.7		
	Friday 10/10	Multiplexers	4.1		
	Friday 10/10	* Midterm Grade Reports Due *			
8	Monday 10/13	Decoders & Encoders	4.2, 4.3	HW 6 due	Lab 6
	Wednesday 10/15	Code Converters	4.4, 4.5		
	Friday 10/11	Latches	5.1, 5.2, 5.3		
9	Monday 10/20	D Flip-Flops	5.4	HW 7 due	Lab 7
	Wednesday 10/22	T Flip-Flops & JK Flip-Flops	5.5, 5.6, 5.7		
	Friday 10/24	Registers	5.8		

Week	Day/Date		Topic	Readings	Homework	Lab
10	Monday	10/27	Register Files	5.8	HW 8 due	Lab 8
	Wednesday	10/29	Counters	5.9, 5.10		
	Friday	10/31	Midterm # 2			
	Friday	10/31	***DROP DEADLINE***			
11	Monday	11/3	Examples with Counters	5.11, 5.14, 5.17	HW 9 due	Lab 9
	Wednesday	11/5	Basic Design Steps	6.1		
	Friday	11/7	State-Assignment Problem	6.2		
12	Monday	11/10	Moore & Mealy Machines	6.3, 6.4	HW 10 due	Lab 10
	Wednesday	11/12	Serial Adder & Arbiter Circuit	6.5, 6.8		
	Friday	11/14	State Minimization	6.6		
13	Monday	11/17	Designing a Counter	6.7	HW 11 due	Lab 11
	Wednesday	11/19	Analysis of Syn. Seq. Circuits	6.9		
	Friday	11/21	ASM charts & Examples	6.10 – 6.13		
14	Monday	11/24	NO CLASS: Thanksgiving Break			No Lab
	Wednesday	11/26	NO CLASS: Thanksgiving Break			
	Friday	11/28	NO CLASS: Thanksgiving Break			
15	Monday	12/1	Register Machines		HW 12 due	Lab 12
	Wednesday	12/3	The i281 CPU			
	Friday	12/5	i281 Assembly Language			
16	Monday	12/8	Arithmetic Logic Unit & PC		HW 13 due	Lab 13
	Wednesday	12/10	Software-Hardware Interface			
	Friday	12/12	Assembly Examples			
17	Wednesday	12/17	FINAL EXAM @ 2:15-4:15 pm	everything		No Lab