Cpr E 281 MINI PROJECT ELECTRICAL AND COMPUTER ENGINEERING

IOWA STATE UNIVERSITY

Mini Project Report

Name and Student ID:	Lab Section:
Date:	
Submission Instructions:	
Prelab:	

- 1 (
 - 1. Complete the prelab
 - 2. Submit this report with the prelab completed to Canvas **before** your lab starts

Lab:

- 1. Complete the lab according to the instructions
- 2. Take screenshots of your ModelSim waveform (note: to receive points your NetID has to be present in the screenshot) and insert them into this document.
- 3. Include screenshots of any related block design files or Verilog files in the report
- 4. Complete this report and reupload it to Canvas

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PRELAB:

Read the Mini-Project lab document and complete as much of this answer sheet as you can before lab.

LAB:

4.0 Draw Uncle Bob's circuit below, using only AND, OR, and NOT gates.

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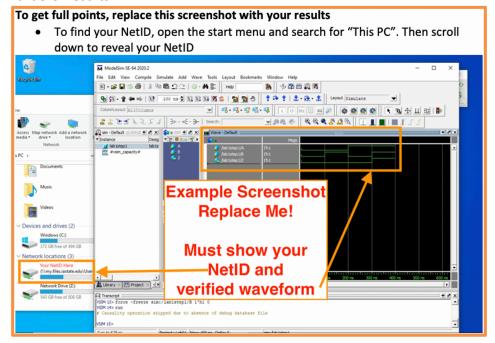
5.0 Give the shorthand canonical SOP expression for Uncle Bob's circuit and write the Verilog code which implements this behavior:

$$B(W, X, Y, Z) = \underline{\hspace{1cm}}$$

Verilog:

<<<Insert a screenshot of your Verilog file>>>

Part 5.0 Results:



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6.0 Truth table for Uncle Bob's function B and the 4-bit prime detector function P.

6.0 Truth table for Uncle Bob's fur							
W	Х	Y	Z	В	Р		
0	0	0	0				
0	0	0	1				
0	0	1	0				
0	0	1	1				
0	1	0	0				
0	1	0	1				
0	1	1	0				
0	1	1	1				
1	0	0	0				
1	0	0	1				
1	0	1	0				
1	0	1	1				
1	1	0	0				
1	1	0	1				
1	1	1	0				
1	1	1	1				

Р					
YZ	X <u>0(</u>)	01	11	10
0					
0	1				
1	1				
1	0				

Simplified SOP Expression:

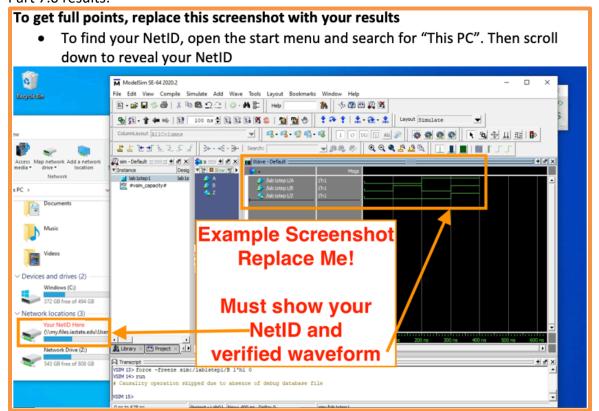
P(W, X, Y, Z) = _____

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7.0 Give your implementation of the correct 4-bit prime detector circuit (**P**) below as either Verilog or a schematic (your choice). Then demonstrate the results:

<<<Insert a screenshot of your BDF/Verilog file here>>>

Part 7.0 results:



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8.0 Design and implement a circuit that uses Uncle Bob's circuit but fixes his mistakes. Draw it below and demonstrate the results:

<<<Insert a screenshot of your circuit drawing here>>>

Part 8.0 results

