CprE 281: Class Schedule (Fall 2020)

Week	Day/Date		Topic	Readings	Homework	Lab
1	Monday	8/17	Introduction	1.1, 1.2, 1.3, 1.4		No Lab
	Wednesday	8/19	Binary Numbers	1.5, 1.6		
	Friday	8/21	Truth Tables & Logic Gates	2.1, 2.2, 2.3, 2.4		
2	Monday	8/24	Boolean Algebra	2.5	HW 1 due	Lab 1
	Wednesday	8/26	AND, OR, NOT	2.6		
	Friday	8/28	NAND, NOR	2.7		
3	Monday	8/31	Design Examples	2.8	HW 2 due	Lab 2
	Wednesday	9/2	Intro to Verilog	2.9, 2.10		
	Friday	9/4	Karnaugh Maps	2.11		
4	Monday	9/7	Minimization	2.12, 2.13	HW 3 due	Lab 3
	Wednesday	9/9	Functions and Circuits	2.14, 2.15, 2.16		
	Friday	9/11	Examples	2.17		
5	Monday	9/14	Addition of Unsigned Numbers	3.1, 3.2	HW 4 due	Lab 4
	Wednesday	9/16	Signed Numbers	3.3		
	Friday	9/18	Midterm #1			
6	Monday	9/21	Fast Adders	3.4, 3.5	HW 5 due	Lab 5
	Wednesday	9/23	Multiplication	3.6		
	Friday	9/25	Floating Point Numbers	3.7		
7	Monday	9/28	Multiplexers	4.1		Mini
	Wednesday	9/30	Decoders & Encoders	4.2, 4.3		Project
	Friday	10/2	Code Converters	4.4, 4.5		
8	Monday	10/5	Latches	5.1, 5.2, 5.3	HW 6 due	Lab 6
	Wednesday	10/7	D Flip-Flops	5.4		
	Friday	10/9	T Flip-Flops & JK Flip-Flops	5.5, 5.6, 5.7		
	Friday	10/9	* Midterm Grade Reports Due *			
9	Monday	10/12	Registers & Register Files	5.8	HW 7 due	Lab 7
	Wednesday	10/14	Counters	5.9, 5.10		
	Friday	10/16	Examples with Counters	5.11, 5.14, 5.17		

Week	Day/Date		Topic	Readings	Homework	Lab
10	Monday	10/19	Basic Design Steps	6.1	HW 8 due	Lab 8
	Wednesday	10/21	State-Assignment Problem	6.2		
	Friday	10/23	Midterm # 2			
	Friday	10/23	***DROP DEADLINE***			
11	Monday	10/26	Moore & Mealy Machines	6.3, 6.4	HW 9 due	Lab 9
	Wednesday	10/28	Serial Adder & Arbiter Circuit	6.5, 6.8		
	Friday	10/30	State Minimization	6.6		
12	Monday	11/2	Designing a Counter	6.7	HW 10 due	Lab 10
	Wednesday	11/4	Analysis of Syn. Seq. Circuits	6.9		
	Friday	11/6	ASM charts & Examples	6.10 – 6.13		
13	Monday	11/9	Register Machines		HW 11 due	Lab 11
	Wednesday	11/11	The i281 CPU			
	Friday	11/13	i281 Assembly Language			
14	Monday	11/16	Arithmetic Logic Unit & PC		HW 12 due	Lab 12
	Wednesday	11/18	Software-Hardware Interface			
	Friday	11/20	Final Review Session			
14	Saturday	11/21	FINAL EXAM @ 2:15-4:15pm	everything		No Lab