

Name and Student ID: _____ Lab Section: _____

Date: _____

PRELAB:

Q1. Read section 3.0 and fill in the truth table below for *lab2step1*. Then use it to construct the SOP expression and draw the resulting circuit using logic gates.

A	B	C	F
0	0	0	
0	0	1	
0	1	0	
0	1	1	
1	0	0	
1	0	1	
1	1	0	
1	1	1	

SOP Logic Expression: _____

Circuit Diagram:

TA Initials: _____

Q2. Read section 4.0 and fill in the truth table below for *lab2step2*. Then use it to construct the SOP expression and draw the resulting circuit using logic gates.

Cabbage	Goat	Wolf	Alarm
0	0	0	
0	0	1	
0	1	0	
0	1	1	
1	0	0	
1	0	1	
1	1	0	
1	1	1	

SOP Logic Expression: _____

Circuit Diagram:

TA Initials: _____

LAB:

3.0 Hardware results demonstrate a correct circuit. TA Initials: _____

4.0 Hardware results demonstrate a correct circuit. TA Initials: _____