

CprE 281: Class Schedule (Fall 2013)

Week	Day/Date	Topic	Readings	Homework	Lab
1	Monday 8/26	Introduction	1.1, 1.2, 1.3, 1.4		No Lab
	Wednesday 8/28	Binary Numbers	1.5, 1.6		
	Friday 8/30	Truth Tables & Logic Gates	2.1, 2.2, 2.3, 2.4		
2	Monday 9/2	NO CLASS: Labor Day			Lab 1
	Wednesday 9/4	Boolean Algebra	2.5	HW 1 due	
	Friday 9/6	AND, OR, NOT	2.6		
3	Monday 9/9	NAND, NOR	2.7	HW 2 due	Lab 2
	Wednesday 9/11	Design Examples	2.8		
	Friday 9/13	Intro to Verilog (part 1)	2.9, 2.10		
4	Monday 9/16	Intro to Verilog (part 2)	2.9, 2.10	HW 3 due	Lab 3
	Wednesday 9/18	Karnaugh Maps	2.11		
	Friday 9/20	Minimization	2.12, 2.13		
5	Monday 9/23	Functions and Circuits	2.14, 2.15, 2.16	HW 4 due	Lab 4
	Wednesday 9/25	Examples	2.17		
	Friday 9/27	Midterm Review Session			
6	Monday 9/30	Midterm #1			Lab 5
	Wednesday 10/2	Addition of Unsigned Numbers	3.1, 3.2		
	Friday 10/4	Signed Numbers	3.3		
7	Monday 10/7	Fast Adders	3.4, 3.5	HW 5 due	Mini Project
	Wednesday 10/9	Multiplication	3.6		
	Friday 10/11	Floating Point Numbers	3.7		
8	Monday 10/14	Multiplexers	4.1	HW 6 due	Lab 6
	Wednesday 10/16	Decoders & Encoders	4.2, 4.3		
	Friday 10/18	Code Converters	4.4, 4.5		
	Friday 10/18	* Midterm Grade Reports Due *			
9	Monday 10/21	Latches	5.1, 5.2, 5.3	HW 7 due	Lab 7
	Wednesday 10/23	D Flip-Flops	5.4		
	Friday 10/25	Midterm Review Session			

Week	Day/Date	Topic	Readings	Homework	Lab
10	Monday 10/28	Midterm # 2			Lab 8
	Wednesday 10/30	T Flip-Flops	5.5		
	Friday 11/1	JK Flip-Flops	5.6, 5.7		
	Friday 11/1	***DROP DEADLINE***			
11	Monday 11/4	Registers	5.8		Lab 9
	Wednesday 11/6	Counters	5.9		
	Friday 11/8	Counters	5.10, 5.11		
12	Monday 11/11	Basic Design Steps	6.1	HW 8 due	Lab 10
	Wednesday 11/13	State-Assignment Problem	6.2		
	Friday 11/15	Mealy State Model	6.3		
13	Monday 11/18	Finite State Machines	6.4	HW 9 due	Lab 11
	Wednesday 11/20	Serial Adder	6.5		
	Friday 11/22	State Minimization	6.6		
14	Monday 11/25	NO CLASS: Thanksgiving Break			No Lab
	Wednesday 11/27	NO CLASS: Thanksgiving Break			
	Friday 11/29	NO CLASS: Thanksgiving Break			
15	Monday 12/2	Designing a Counter	6.7	HW 10 Due	Final Project
	Wednesday 12/4	Analysis of Synchronous Seq. Circuits	6.8, 6.9		
	Friday 12/6	ASM charts	6.10, 6.11,		
16	Monday 12/9	Examples	6.12, 6.13	HW 11 due	Final Project Demo
	Wednesday 12/11	TBD			
	Friday 12/13	Final Review Session			
17	Finals Week 12/??	FINAL EXAM @ (time TBD)	Ch. 1-6		No Lab