PARALLEL COMPUTING HEURISTICS FOR LOW-RANK MATRIX COMPLETION

Charlie Hubbard and Chinmay Hegde

Electrical and Computer Engineering Department
Iowa State University, Ames, IA, USA 50010

ABSTRACT

Current algorithms for low-rank matrix completion often suffer from scalability issues — both in terms of memory as well as running time — when presented with very large datasets. In this paper, we introduce new parallel computing heuristics that can greatly accelerate matrix completion algorithms when used in GPU-based computing environments. Our heuristics enable speeding up popular algorithms for nonlinear matrix completion on standard real-world test datasets by orders of magnitude, while being highly memory-efficient.

1. INTRODUCTION

Motivation. The problem of recovering a data matrix from a small sample of its entries, also called the matrix completion problem, arises in several real-world applications including content recommender systems, sensor localization, and system identification.

For real-world applications, this problem poses several challenges. First, the application of standard approaches for matrix completion to the problem of content recommendation is not seamless. The matrix completion literature assumes that the entries of the matrix are real-valued; however, the ratings provided by users of content providers are almost always “quantized” to some finite set of integers. To remedy this, the use of 1-Bit matrix completion [1] has been shown to outperform contemporary matrix completion methods by intrinsically modeling the ratings as non-numeric entities.

Second, standard matrix completion algorithms do not leverage any “side-information” known about the rows (users) and items (columns) of a given matrix. To remedy this, a new framework known as inductive matrix completion (IMC) [2] has been proposed. However, incorporating this extra information expands the memory footprint of the associated algorithms.

In both of the above situations, current state-of-the-art methods suffer from scalability issues. For example, in collaborative filtering applications used by content providers such as Netflix or Amazon, the number of users and/or items can both be in the order of hundreds of millions. In order to perform efficient matrix completion for very large datasets, parallelization is paramount. The question to be asked is: how best to leverage modern parallel computing environments for matrix completion methods?

Our contributions. In this paper, we introduce two new parallel computing heuristics for solving matrix completion problems. Our heuristics, that we call GPUFISH and IMCFISH, are modular, tunable, and leverage the massive number of multiple concurrent kernel executions possible on a modern GPU. As stylized applications, we demonstrate how to adapt GPUFISH to solve the 1-bit matrix completion problem where the matrix observations are binary [1]. Our results demonstrate that we achieve a 150x speedup over existing serial algorithms, while maintaining comparable prediction accuracy. Our work demonstrates that a standard workstation equipped with a single GPU can be effectively deployed to solve very large scale matrix completion problems. We also demonstrate the use of IMCFISH to solve inductive matrix completion problems. For very large IMC problems IMCFISH is able to obtain competitive solutions while only having access to a fraction of the dataset.

Our work is open-source and a CUDA implementation of our proposed heuristics is available at https://github.com/ehubbard/gpu-fish.

Our techniques. The algorithmic core of GPUFISH is an optimized implementation of the JELLYFISH framework [3]. Similar to the approach proposed in [3], our approach also employs a randomized, incremental stochastic gradient descent approach. However, GPUFISH generalizes the previous approach in two distinct ways: (i) GPUFISH enables the user to transparently adapt to domain-dependent problems, thus extending the matrix completion framework to numeric as well as non-numeric observations. (ii) GPUFISH enables the user to leverage the full parallel processing power of a GPU, and can concurrently process hundreds of available samples in training.

For inductive matrix completion (IMC) problems, a straightforward application of the above parallelization scheme does not work. Instead, our proposed heuristic IMCFISH uses a partial gradient descent scheme that enables parallel updates in a manner similar to JELLYFISH. This gradient descent scheme relies on a novel data partitioning approach that we call striping. Together with the parallelization heuristics described above, this leads to improved performance.

Relation to prior work. The recent, large body of work in matrix completion has shown that as long as the matrix M possesses sufficiently low rank, we can recover the missing entries of M via a convex optimization procedure [4–7]. However, convex optimization approaches are not particularly suitable for matrices larger than a few hundred rows/columns. To resolve this, a non-convex, incremental heuristic for matrix completion was introduced in [3].

The problem of completing a matrix whilst taking into account the available rows/column features is known as inductive matrix completion (IMC). Theory, applications, and expansions of IMC are explored in [2, 8–10]. However, to our knowledge there have been no works that explore the effect of parallelization for IMC algorithms.

Finally, a very recent work [11] advocates a new algorithm for GPU-based matrix completion based on cyclic coordinate descent (CCD). Our method differs in two respects: we consider the more complicated problems of 1-bit matrix completion as well as inductive matrix completion, and our update rules involve large portions of the matrix variables (as opposed to individual coordinates). Due to space (and time) constraints, we defer a thorough comparison to future work.
2. PARALLEL 1-BIT MATRIX COMPLETION

As our first application, we describe an instantiation of GPUFISH for solving large scale instances of the matrix completion problem where the user ratings are available in the form of binary (like/dislike) observations. We adopt the 1-bit matrix completion model of [1]. The goal is to fill in any missing entries of a rank-$r$ matrix $\mathbf{M}$ with $n_r$ rows and $n_c$ columns. However, in a departure from classical matrix completion, we do not get to directly observe the entries of $\mathbf{M}$. Instead, consider any twice-differentiable function $f : \mathbb{R} \to [0, 1]$. We record observations $\mathbf{Y}$ such that:

$$ Y_{i,j} = \begin{cases} +1 & \text{with probability } p(M_{i,j}), \\ -1 & \text{with probability } 1 - p(M_{i,j}), \end{cases} \text{ for } (i,j) \in \Omega. $$

(2.1)

As with previous work in matrix completion, it is important that $\Omega$ is chosen uniformly at random. In [1], the Probit and Logit functions are explored as natural functions to model the underlying distribution $p(\cdot)$ of the entries of $\mathbf{Y}$. In this work, we focus on the Logit function $f(x) = \frac{e^x}{1 + e^x}$. To recover an estimate of $\mathbf{M}$ we can maximize the log-likelihood function of the optimization variable $\mathbf{X}$ over the set of observations $\Omega$. Denote $\|A\|$ as the indicator function over a Boolean condition $A$. Then the log-likelihood function corresponding to the Logit model is given by:

$$ \mathcal{L}(\mathbf{X}) := \sum_{(i,j)\in \Omega} \left[ Y_{i,j} + 1 \log(p(X_{i,j})) + 1 \log(1 - p(X_{i,j})) \right] \quad (2.2) $$

The estimate of $\mathbf{M}$, therefore, is given by the solution to the constrained optimization problem$^1$:

$$ \hat{\mathbf{M}} = \arg\max_{\mathbf{X}} \mathcal{L}(\mathbf{X}), \text{ rank}(\mathbf{X}) \leq r. \quad (2.3) $$

The optimization problem (2.3) is non-convex, due to the presence of the rank constraint on $\mathbf{X}$. The standard method adopted in matrix completion approaches is to perform a nuclear norm relaxation of the rank constraint. However, nuclear norm-regularized matrix recovery formulations can incur a high running time [3,4,6]. In order to resolve this issue, we adopt the JELLYFISH approach of [3]. We factorize the variable $\mathbf{X}$ into two variables $\mathbf{L}$ and $\mathbf{R}$ and obtain the bilinear optimization problem:

$$ \min \mathcal{L}(\mathbf{LR}^t) \text{ s.t. } \|\mathbf{L}\|_2^2 \leq B, \|\mathbf{R}\|_2^2 \leq B. \quad (2.4) $$

To solve (2.4), we adopt an incremental projected gradient descent approach. We alternately update $\mathbf{L}$ (resp., $\mathbf{R}$) while keeping $\mathbf{R}$ (resp., $\mathbf{L}$) fixed. In each iteration, the updates to the $i^{th}$ row of $\mathbf{L}$ and the $j^{th}$ row of $\mathbf{R}$ are given by:

$$ \mathbf{L}_{ik}^{(k+1)} = \Pi_B \left( \mathbf{L}_{ik} - \alpha_k \mathcal{L}'(\mathbf{L}^{(k)}_{ik}) \mathbf{R}_{jk}^{(k)} \right) $$

$$ \mathbf{R}_{jk}^{(k+1)} = \Pi_B \left( \mathbf{R}_{jk} - \alpha_k \mathcal{L}'(\mathbf{L}^{(k)}_{ik}) \mathbf{R}_{jk}^{(k)} \right) \quad (2.5) $$

where the projection operator $\Pi$ onto the constraint set in (2.4) admits the closed form expression $\Pi_B(v) = \frac{\sqrt{\|v\|}}{\|v\|} v$ if $\|v\|^2 \geq B$ and $v$ otherwise. The step size parameter $\alpha_k$ decreases by a constant amount at every iteration.

The above gradient descent method has several computational advantages. We observe that the updates performed in (2.5) operate on highly local portions of the matrices $\mathbf{L}$ and $\mathbf{R}$. Therefore, if the observed data points in $\Omega$ were suitably partitioned into non-overlapping blocks, then each block can be independently processed. We will refer to these blocks as chunks and index any chunk $C_{a,b}$ where $a$ and $b$ are row and column indices of the chunk in the partitioned matrix. Moreover, if we have the means to process $p$ chunks in parallel, then we can divide $\mathbf{M}$ into $p^2$ chunks, and further group the chunks into $p$ rounds. Now, we can process each round sequentially, such that no two parallel processes will ever be manipulating the same rows of $\mathbf{R}$ or $\mathbf{L}$ at the same time, thus eschewing any locking delays$^2$. This is the intuition exploited in [3]; however, they assume a standard multi-core CPU-based computing model, and also do not consider the problem of nonlinear (one-bit) matrix completion.

**GPUFISH: Parallel matrix completion on GPU.** We provide a high level description of the organization of a GPU. Each process instantiated on the GPU is known as a kernel. A kernel can be executed in parallel across several threads of the GPU. A compiler hierarchically groups the parallel threads into blocks, and blocks into a grid of blocks. When launching a kernel on the GPU, the user controls the number of blocks to launch as well as the number of threads per block to launch. Each thread launched by the kernel executes an instance of that kernel. Threads in a block execute concurrently.

We now leverage this special architecture for our incremental gradient descent algorithm. Suppose that we have divided $\Omega$ into $p^2$ chunks. We launch a single kernel for each of the $p$ rounds we have created. As noted above, the kernels must be launched sequentially to perform the parallel updates without fine-grained locking. Each round will contain $p$ chunks so we will instantiate our kernel with $p$ blocks. Each block is responsible for performing gradient updates (2.5) for all data points (samples) in the corresponding chunk. Each block of the kernel contains $r$ worker threads; each thread, $t_k$, in a given block is responsible for updating $\mathbf{L}_{ik}$ and $\mathbf{R}_{jk}$, that is, the $k^{th}$ entry in the rows of $\mathbf{L}$ and $\mathbf{R}$ being updated by (2.5) according to the data point $(i,j,Y_{i,j})$. In this way, we not only perform the gradient updates for a large number of data points, but also update in parallel the $r$ entries of any row of $\mathbf{L}$ or $\mathbf{R}$. Therefore, using this procedure we get an $r$-fold speedup per round over the JELLYFISH algorithm.

We can further optimize running time as follows. While a given kernel (corresponding to one of the rounds) is being processed by the GPU, we simultaneously loading the data required to execute the next kernel onto the GPU. At the completion of the given we remove its data from the GPU and continue to the next round. The process of chunking our data and then performing parallel gradient updates over $p$ kernels is known as an epoch. Because each epoch requires a new shuffle of our dataset, we begin each epoch by launching a separate CPU thread to compute the shuffle required for the next epoch; this extra CPU thread is executed in parallel with the GPU kernel launches being handled by the main CPU.

Each of the above optimization heuristics are specific to GPU-based computing and enable considerable improvements in running time over the standard JellyFish algorithm. Therefore, we call our modified approach GPUFISH, summarized as Algorithm 1.

We discuss some specific schemes for managing the various observations and variables in practical implementations of GPUFISH. Before the first epoch, the matrices $\mathbf{L}$ and $\mathbf{R}$ are loaded into the global memory of the GPU where they can be accessed by all threads of the GPU. Thread access to global memory is generally slow, so rather...
Algorithm 1 GPUFish
1: Permute rows and columns of \( M \), shuffle \( \Omega \)
2: Separate \( \Omega \) into \( p^2 \) chunks
3: Round[\( \Omega \)] = \( p \) chunks s.t. all chunks are non-overlapping
4: Transfer data for Round[1] to GPU
5: for \( i = 1 \) to \( p \) in parallel do
6: \( GPU_{\text{Gradient Updates}}(p \text{ blocks}, r \text{ threads per block}) \)
7: Transfer data for Round[\( i+1 \)] to GPU overwriting Round[\( i-1 \)]
8: end for

Algorithm 2 GPU_{\text{Gradient Updates}}
1: for each of \( p \) chunks in parallel do
2: for each data point \((i, j, \text{rating})\) in the chunk do
3: apply (2.5) to \( L \) and \( R \)
4: end for
5: end for

than make repeated calls to global memory we begin by loading the relevant rows of \( L \) and \( R \) into \textit{shared memory} on the GPU. This memory is shared only between the threads of each block and access to it is significantly faster than global memory. After completing our computation of (2.4) from our copies of \( L \) and \( R \) in shared memory we write the our updates to \( L \) and \( R \) back to global memory.

In addition to making use of the GPU’s faster shared memory, we also make use of the ability of the GPU’s ability to transfer data while processing a kernel(s). At the beginning of each epoch we transfer the data needed for the first round of gradient updates and launch the kernel responsible for performing updates on the data. As this kernel processes the first round we gather the data required to process round two and load it onto the GPU. This procedure is repeated until the epoch has finished. While providing an obvious speedup over performing all of the data transfers at once, this data transfer the data needed for the first round of gradient updates and back to global memory.

Proceeding in an identical fashion as (2.5), we can use incremental projected gradient descent to solve (3.1); our updates for each iteration are:

\[
L_k^{(k+1)} = \Pi_B \left( L_k - \alpha_k \mathcal{L}'(A_k L_k^{(k)} B_k^*) A_k^* B_k R_k^{(k)} \right)
\]

\[
R_k^{(k+1)} = \Pi_B \left( R_k - \alpha_k \mathcal{L}'(A_k L_k^{(k)} B_k^*) B_k^* A_k L_k^{(k)} \right)
\]

(3.2)

The updates in (3.2) differ from the standard one-bit matrix completion problem in a rather fundamental fashion. Crucially, the gradient updates are no longer local; every incremental update step affects all of \( L \) and all of \( R \), in sharp contrast with our earlier case. Therefore, existing “block”-based data partitioning techniques (such as that proposed in [3]) are no longer applicable. We resolve this issue using a novel algorithm described as follows.

IMCFish: Parallel inductive matrix completion. We first introduce the notion of striping. Suppose we choose an integer parameter \( s \) (for simplicity, a divisor of both \( m \) and \( n \)); then, the \( q^{th} \) stripe of a vector \( v \in \mathbb{R}^{1 \times n} \), denoted \( v_q \), is given by: \( v_q[k] = 0 \) for \( k < q \ast (m/s) \) and \( k \geq (q + 1) \ast (m/s) \), and \( v[k] \) otherwise. If we multiply the transpose of our striped vector, \( v^* \in \mathbb{R}^{n \times 1} \), with some other vector \( t \in \mathbb{R}^{1 \times m} \) we observe that the product of these two vectors, \( Y \), is a striped matrix (see Fig. 1); a row of \( Y \) is zero if the corresponding row of \( v^* \) is zero. Using this knowledge we can rewrite our updates (3.3) in terms of stripes of \( A \) and \( B \):

\[
L_k^{(k+1)} = \Pi_B \left( L_k - \alpha_k f'(A_k L_k^{(k)} B_k^*) A_k^* B_k R_k^{(k)} \right)
\]

\[
R_k^{(k+1)} = \Pi_B \left( R_k - \alpha_k f'(A_k L_k^{(k)} B_k^*) B_k^* A_k L_k^{(k)} \right)
\]

(3.3)

In this way, we are able to confine our gradient updates to local portions of \( L \) and \( R \) allowing us to do many updates in parallel. We call this algorithm IMCFish, and omit its pseudocode description due to space constraints.

We now describe an efficient GPU-based implementation of IMCFish. Examining (3.3), we find that while our updates to \( A \) are local, \( R \) is not. In particular, a single striped row of \( B \) will be responsible for performing gradient updates with a unique combination of stripes. As with GPUPERISH, we launch \( q \) GPU kernels each containing \( q \) blocks. The \( q^{th} \) stripe of \( A \) and the \( q^{th} \) stripe of \( B \) used by a single chunk is determined by the index of that block and the current round.

Note that in each gradient update of IMCFish, we only require a small portion of the feature matrices \( A \) and \( B \). In particular, a single gradient update only requires \( \frac{d_2}{q} \) feature values from \( A \) and \( \frac{d_2}{q} \). Given \( n_s \) rows and \( n_c \) columns we require \( \frac{n_s \ast d_2}{q} \) feature values from \( A \) and \( \frac{n_c \ast d_2}{q} \) features from \( B \) to perform all the gradient updates in a
Table 1. A comparison between 1-bit matrix completion from [1] and the 1-bit matrix completion implemented in GPUFISH. GPUFISH produces results on par with the traditional 1-bit approach and is able to do so in a fraction of the runtime.

<table>
<thead>
<tr>
<th>Original Rating</th>
<th>1</th>
<th>2</th>
<th>3</th>
<th>4</th>
<th>5</th>
<th>Overall</th>
<th>Runtime(s)</th>
</tr>
</thead>
<tbody>
<tr>
<td>GPUFISH: ML 100k</td>
<td>80%</td>
<td>77%</td>
<td>58%</td>
<td>71%</td>
<td>87%</td>
<td>72%</td>
<td><strong>0.30</strong></td>
</tr>
<tr>
<td>1-Bit: ML 100k</td>
<td>79%</td>
<td>73%</td>
<td>58%</td>
<td>75%</td>
<td>89%</td>
<td>73%</td>
<td>47</td>
</tr>
<tr>
<td>GPUFISH: ML 1m</td>
<td>86%</td>
<td>74%</td>
<td>55%</td>
<td>75%</td>
<td>92%</td>
<td>74%</td>
<td>1.1</td>
</tr>
<tr>
<td>1-Bit: ML 1m</td>
<td>84%</td>
<td>76%</td>
<td>53%</td>
<td>77%</td>
<td>94%</td>
<td>75%</td>
<td>3130</td>
</tr>
</tbody>
</table>

4. PERFORMANCE EVALUATION

All experiments were performed on a Dell workstation equipped with: a 6-core Xeon E5-2620 v3 CPU, 64GB of RAM, a 256GB Class 30 SSD, and an NVIDIA GeForce GTX 1080 GPU. For our experiments, we use Linux 3.10.0-327 along with NVCC V8.0.26.

**GPUFISH.** We test the ability of GPUFISH to make predictions in the collaborative filtering environment on real-world data. Specifically we make predictions about user interest in movies for the Movielens (100k, 1m and 20m) data set [13]. Where possible we compare our results to those produced from the code released with [1].

We transform the user-movie ratings from the Movielens data set (integers in [1,5]) to one-bit observations by subtracting the average over all ratings (approximately 3.5) from each rating and recording the sign. Our input parameters, including rank, are again determined by a grid search. Each instance of GPUFISH was terminated after 20 epochs. For each Movielens data set (100k, 1M and 20M) we remove 5,000 ratings for testing purposes, and train the model with the remaining ratings. In Table 1 we present the percentage of one-bit ratings correctly recovered by GPUFISH as a function of the original rating. We also display the overall percentage of ratings correctly recovered as well as the runtime of the algorithm.

We empirically determine the number of blocks per kernel (the number of chunks in a round) that results in the smallest run time. The results are presented in Figure 2. For each epoch we perform two processes in parallel: gradient updates on the GPU, and the permuting and chunking $\Omega$; the run time of each epoch is the maximum of the time taken by either of these two processes. Examining Figure 2, we see that executing GPUFISH with a larger number of blocks per kernel can only decrease our runtime to the extent that it is no longer determined by the execution of gradient updates. At approximately 30 blocks per kernel our GPU gradient updates can be performed faster than our permutations and chunking of $\Omega$, and therefore we no longer see a decrease in runtime beyond this level of block granularity.

**IMCFish.** We now test IMCFish on a synthetic matrix dataset. Where possible we compare our results to those produced from the code released with [9].

We present phase transitions for the recovery of a random matrix, $M \in \mathbb{R}^{1000\times 1000}$ with rank-one and rank-ten latent feature spaces $Z \in \mathbb{R}^{50 \times 100}$ and another in $\mathbb{R}^{50 \times 50}$, the entries of both matrices are drawn from the standard normal distribution. To create our random feature matrices $A, B$, we draw from the standard normal distribution two matrices in $\mathbb{R}^{1000 \times 50}$. We obtain $M$ by multiplying our feature matrices with $Z$.

The phase transitions for IMCFISH with 5,10 and 25 stripes are presented in Fig. 3, as are the phase transition for the LEML [14]-based IMC algorithm detailed in [9]. Given the output of IMCFISH $Z$, and the IMC algorithm’s estimate of the latent feature space as the “ground truth” $\hat{Z}$, we compute the relative error of this estimate $||Z - \hat{Z}||^2_F / ||Z||^2_F$. In the same manner as GPUFISH, we manually tune our step size and regularization term for optimal recovery of $Z$. In Fig. 3 the relative error of our recovery is plotted against the fraction of visible entries of $M$. We note that all versions of IMCFISH, though they only use a portion of the feature matrices to perform gradient updates, are able to recover $Z$ with fewer visible entries than the IMC algorithm proposed in [9].

**Acknowledgements.** This work was supported in part by grant #CCF-1566281 from the National Science Foundation and a GPU grant from NVIDIA.
5. REFERENCES


